

Fig. 1

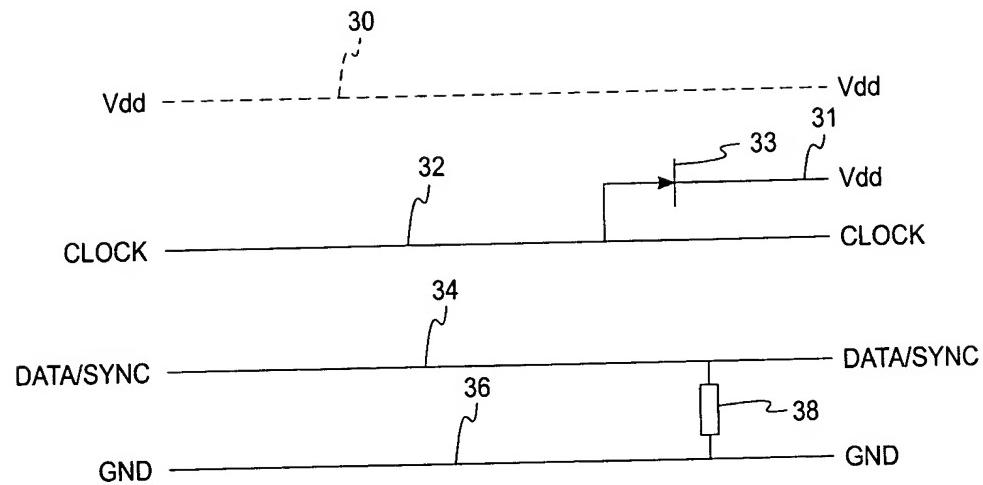
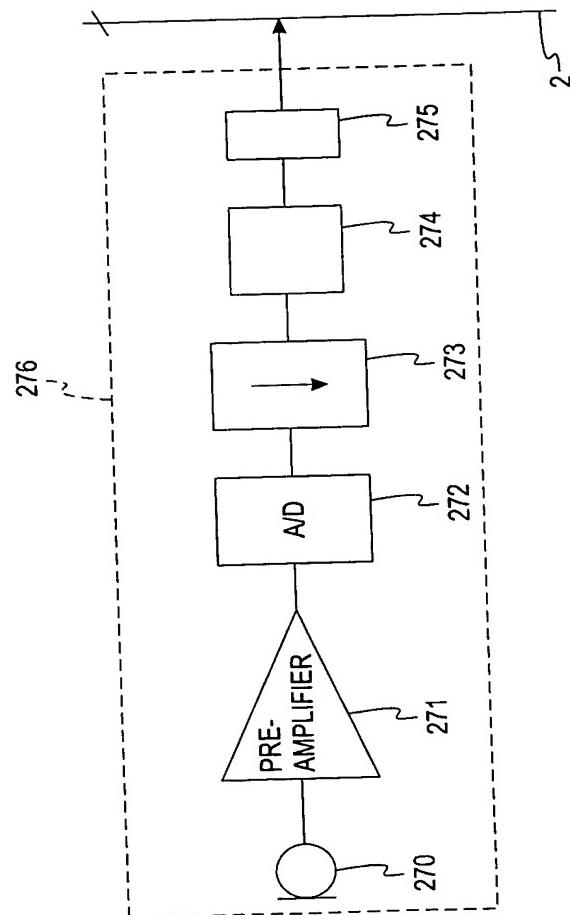


Fig. 2a

Fig. 2b



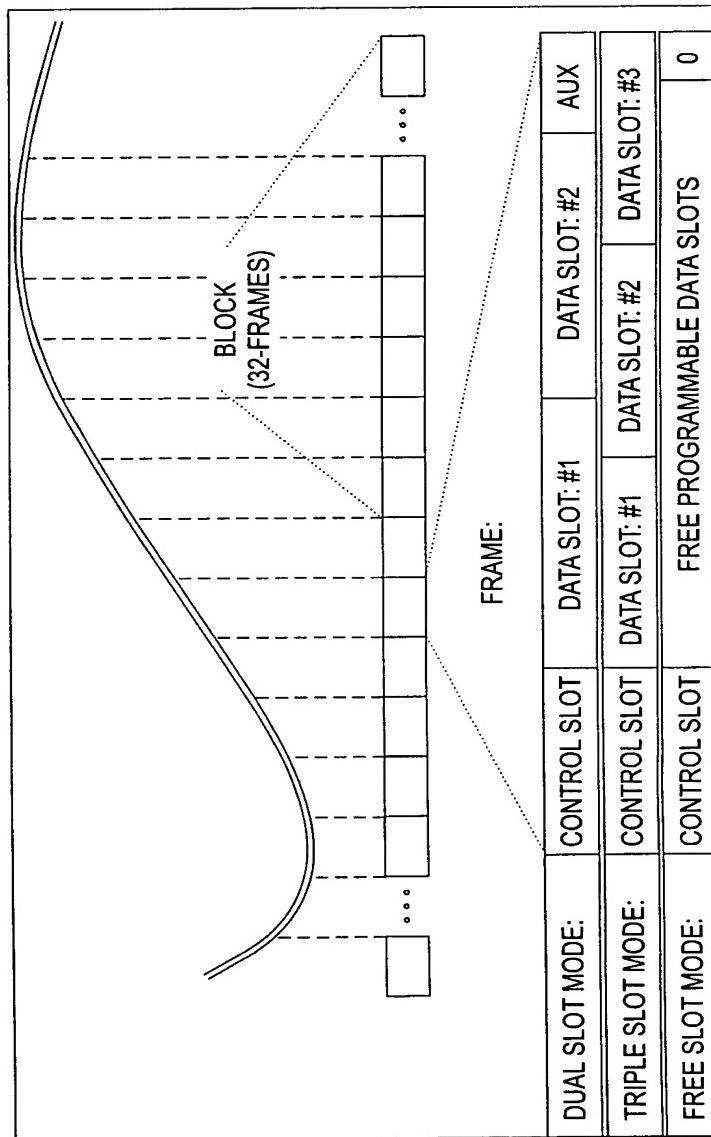


Fig. 3

Fig. 4a

DUAL SLOT MODE						
BUS ERROR	DEVICE PRESENT	FRAME SYNC 1	CONTROL DATA	FRAME SYNC 2	DATA SLOT 1	DATA SLOT 2
1 UNIT	1 UNIT	3 UNITS	1 UNIT	1 UNIT	28 UNITS	28 UNITS

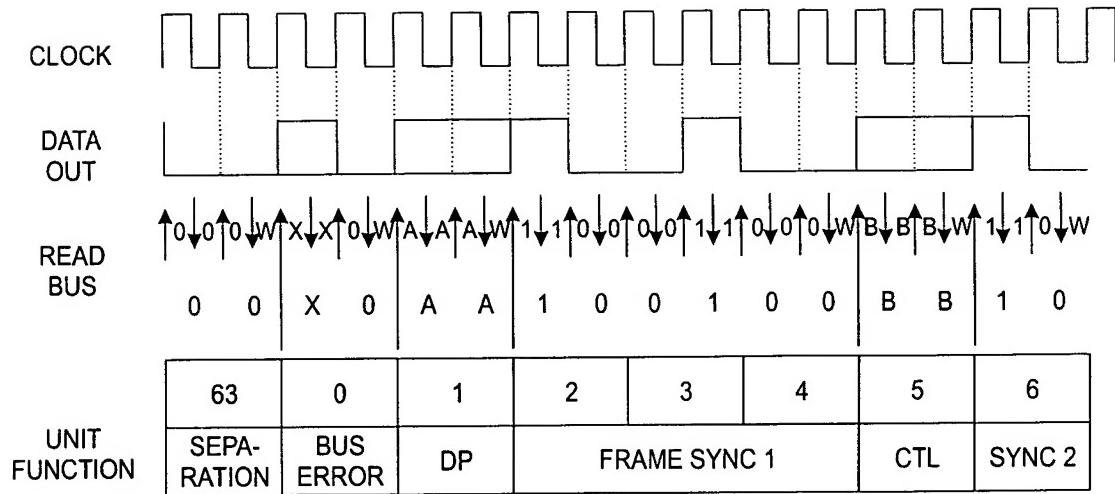
TRIPLE SLOT MODE

TRIPLE SLOT MODE						
BUS ERROR	DEVICE PRESENT	FRAME SYNC 1	CONTROL DATA	FRAME SYNC 2	DATA SLOT 1	DATA SLOT 3
1 UNIT	1 UNIT	3 UNITS	1 UNIT	1 UNIT	19 UNITS	19 UNITS

Fig. 4b

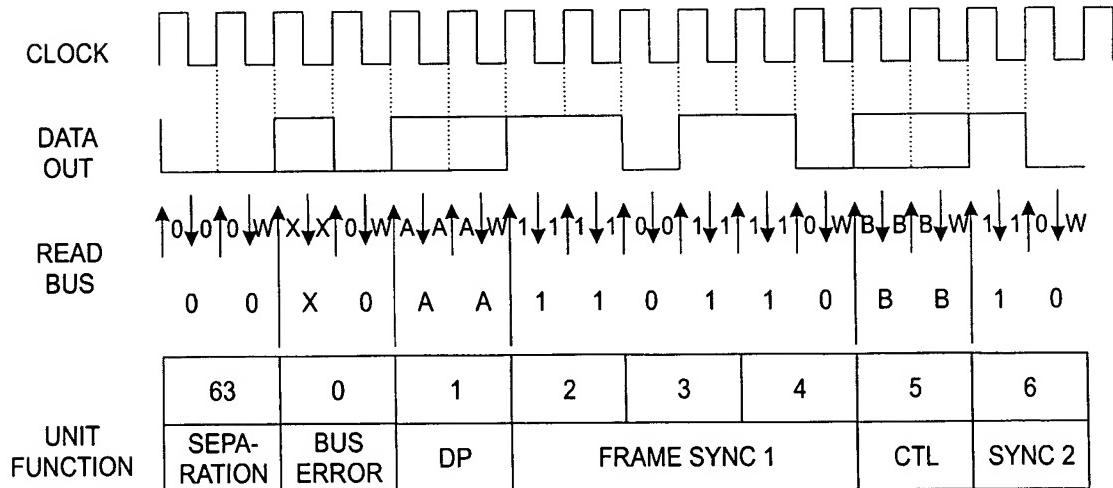
FREE SLOT MODE						
BUS ERROR	DEVICE PRESENT	FRAME SYNC 1	CONTROL DATA	FRAME SYNC 2	FREE PROGRAMMABLE DATA SLOTS	
1 UNIT	1 UNIT	3 UNITS	1 UNIT	1 UNIT	57 UNITS	

Fig. 4c



Legend: 0 = logic level LOW, 1 = logic level HIGH, X = Don't care, W = driver is tri-state, Bus is in a weak pull down.

Fig. 5



Legend: 0 = logic level LOW, 1 = logic level HIGH, X = Don't care, W = driver is tri-state, Bus is in a weak pull down.

Fig. 6

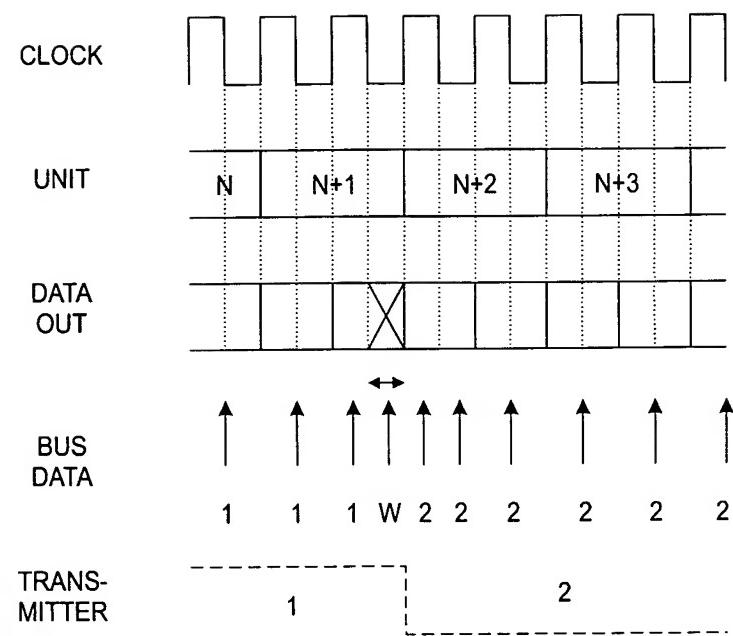


Fig. 7

Fig. 8

INTERRUPT REQUEST	ENABLE INTERRUPT INFORMATION	ADDRESS	COMMAND	R/W	DATA	PARTY
1 BIT	1 BIT	5 BITS	7 BITS	1 BIT	16 BITS	1 BIT
X	0	LSB...MSB	LSB...MSB		LSB...MSB	

Fig. 9

INTERRUPT REQUEST	ENABLE INTERRUPT INFORMATION	DEVICE 1	DEVICE 2	DEVICE 2-29	DEVICE 30
1 BIT	1 BIT	1 BIT	1 BIT	28 BIT	1 BIT
1	1				

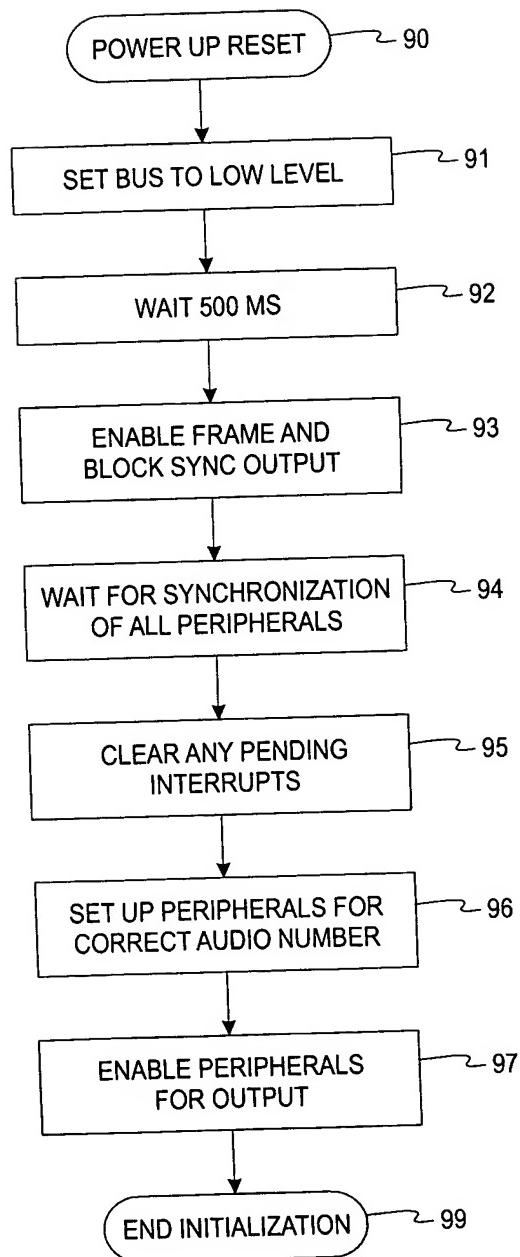
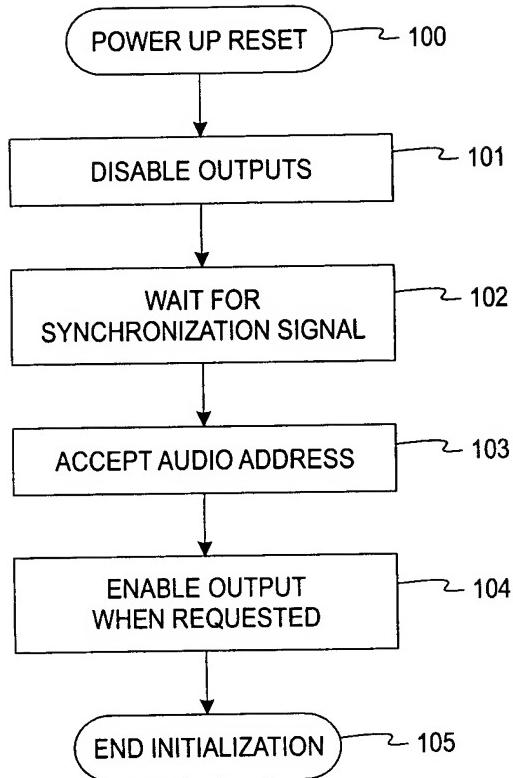
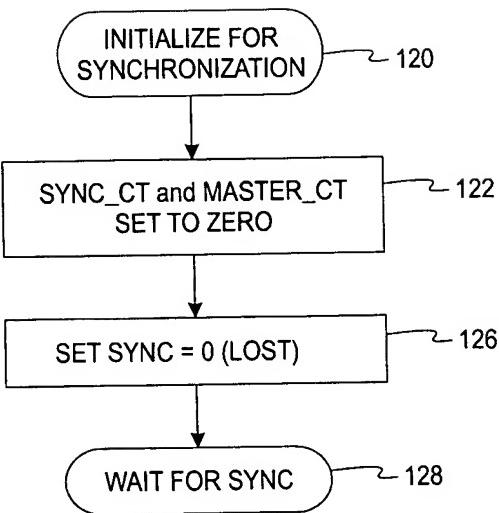


Fig. 10

*Fig. 11**Fig. 12*

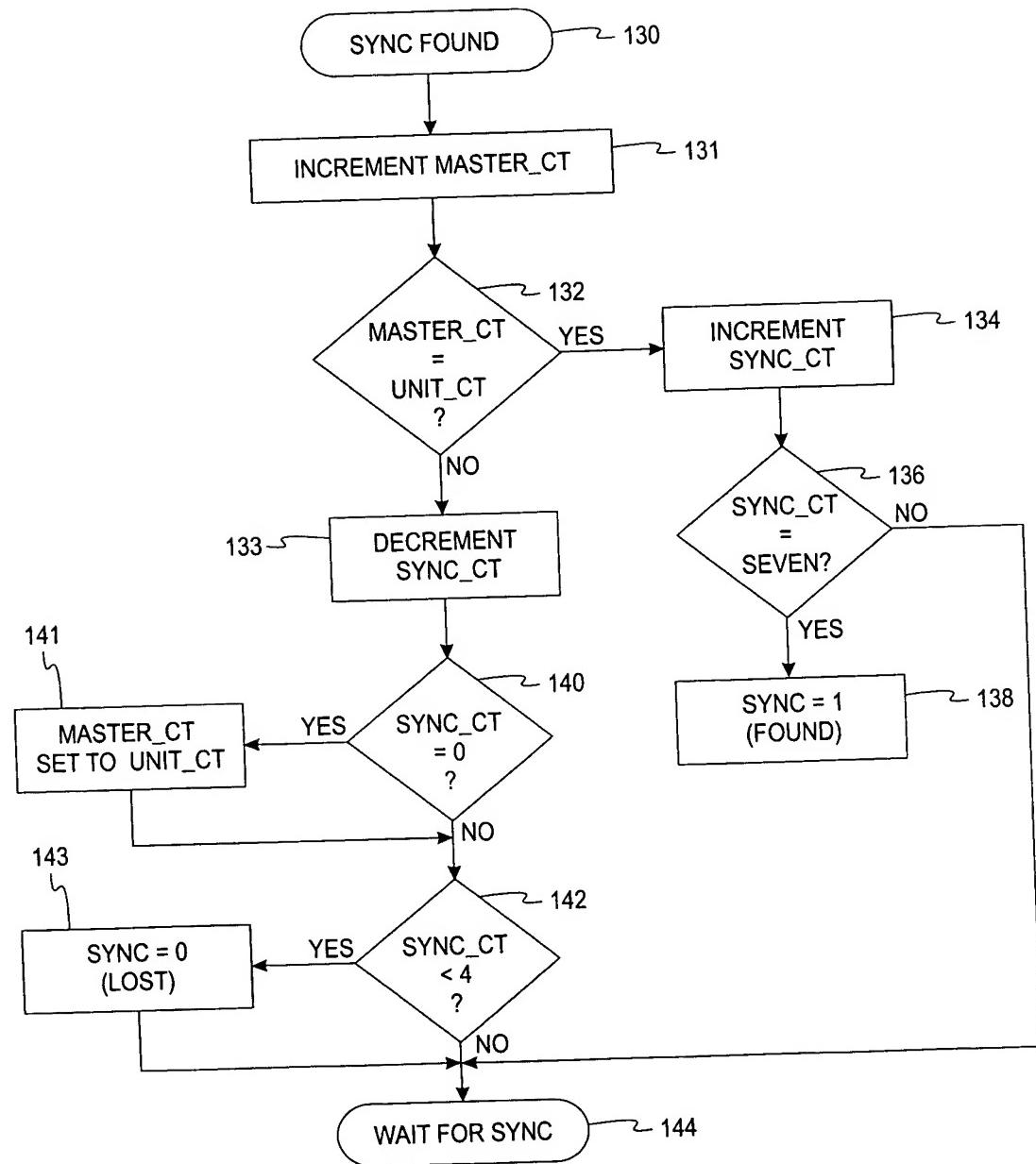


Fig. 13

PARAMETER	UNITS
GENERAL OUTPUT LEVELS	
LOGICAL ZERO	0-15% Vdd
LOGICAL ONE	85-100% Vdd
GENERAL INPUT LEVELS	
LOGICAL ZERO	0-30% Vdd
LOGICAL ONE	70-100% Vdd
LIMITS	
GROUND	0.0 VOLT
Vdd, MIN	0.7 VOLT
Vdd, MAX	1.8 VOLT
SETUP AND HOLD TIME REQUIREMENTS	
MIN TIME TO TRI-STATE	5 ns
MAX TIME TO TRI-STATE	80 ns (TCLOCK/2)
MIN TIME TO EXIT TRI-STATE	0 ns
MAX TIME TO EXIT TRI-STATE	40 ns (TCLOCK/4)
MAX SETUP TIME FOR DATA	40 ns (TCLOCK/4)
MAX HOLD TIME FOR DATA	5 ns

Fig. 14

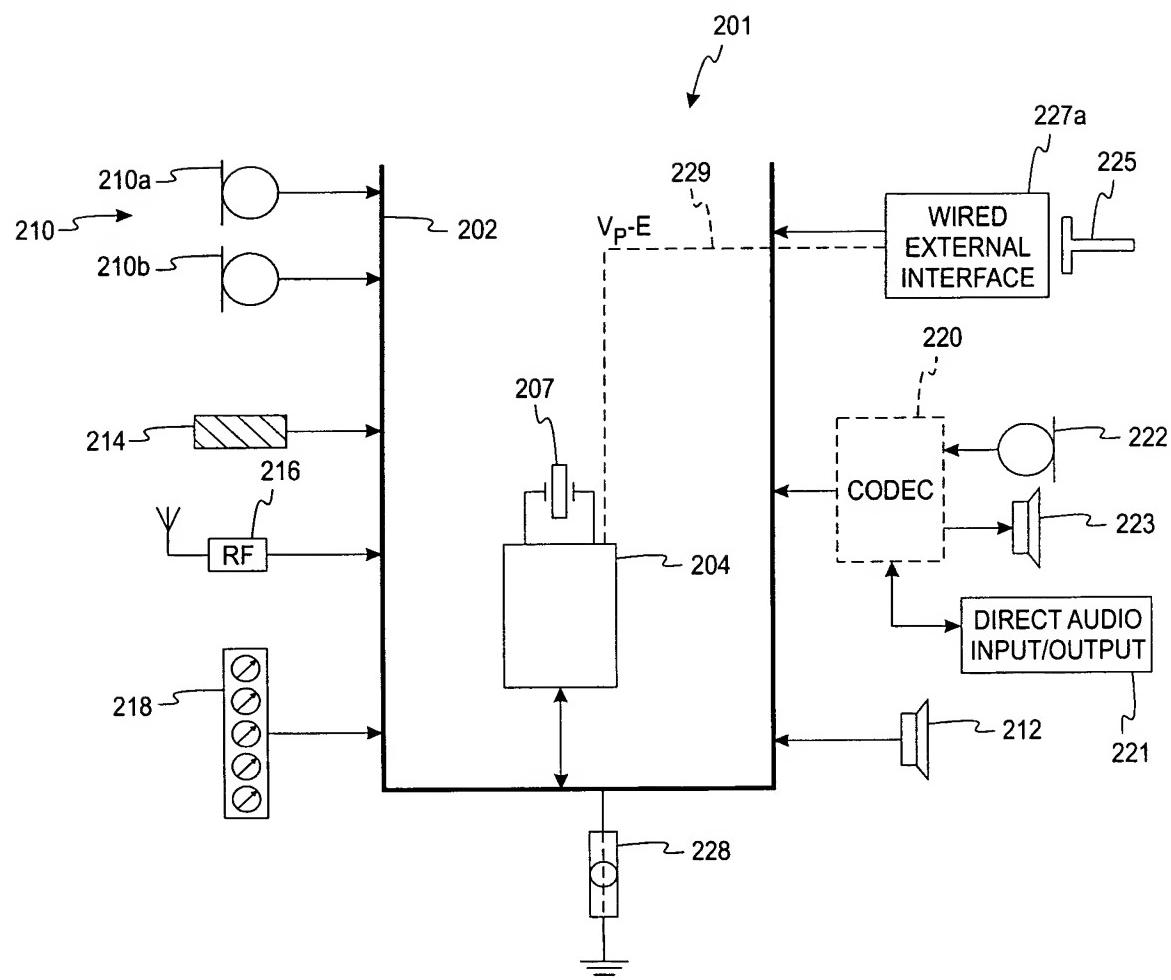


Fig. 15

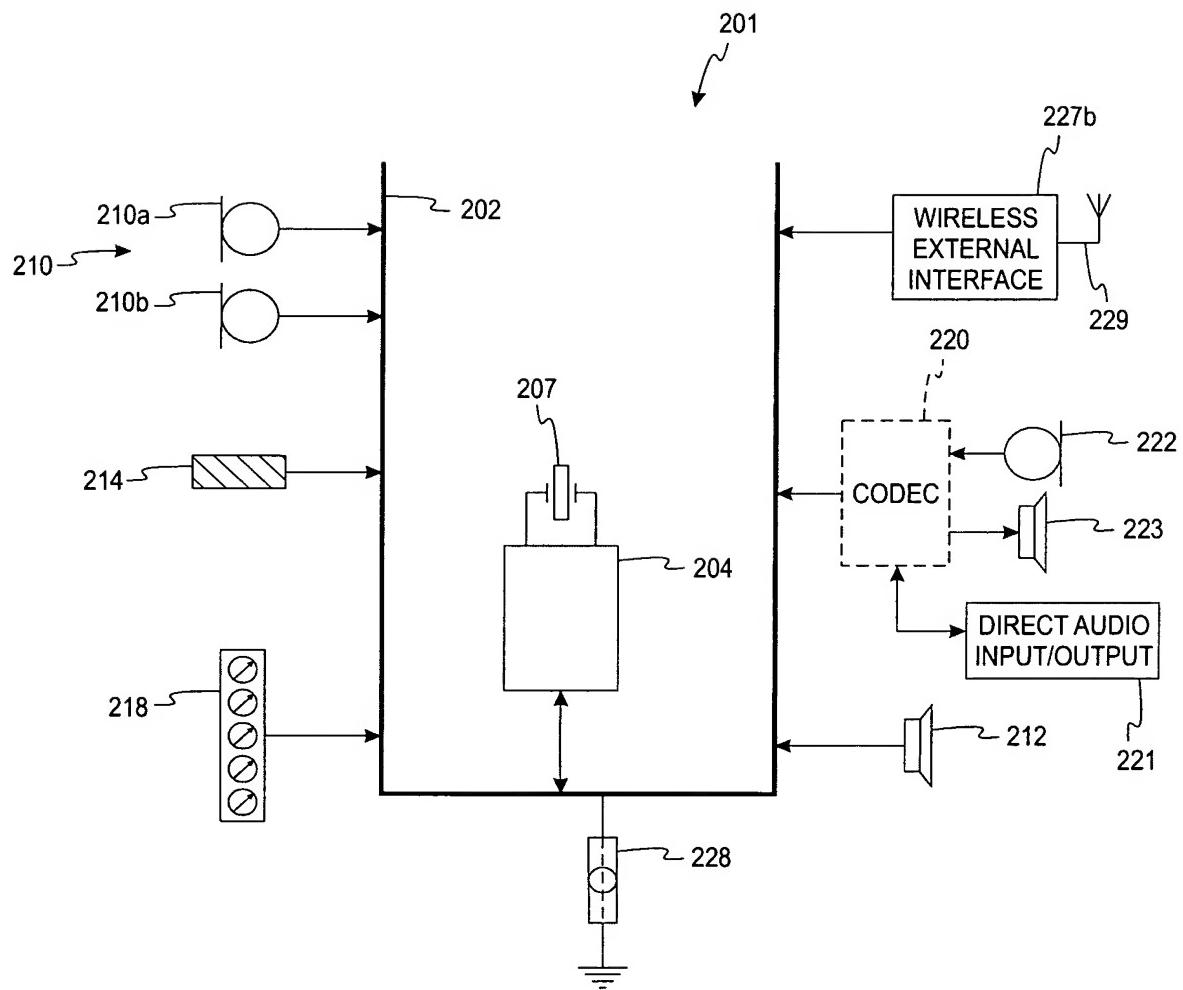


Fig. 16

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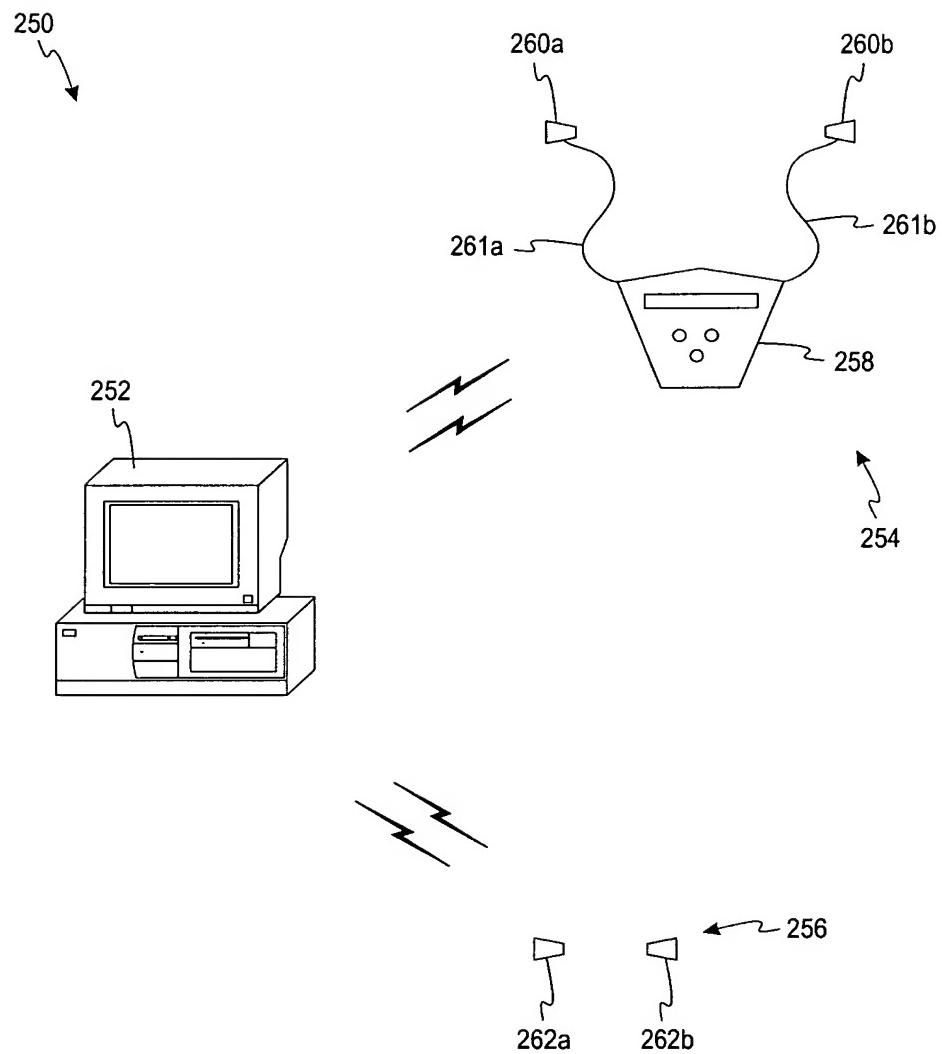


Fig. 17

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